ABSTRACT OF THE DISCLOSURE

A power output apparatus includes two motor generators, a DC power supply, a relay, three inverters and a control CPU. One of the motor generators includes three-phase coils. The DC power supply is connected via the relay between respective neutral points of the three-phase coils. When the sum of respective powers of the motor generators is zero, the control CPU generates a signal SE at L level for rendering the relay OFF to output the generated signal to the relay, and generates signals PWMC1, PWMC2 and PWMI3 for driving the other motor generator by electric power generated by the one motor generator to output the generated signals to the inverters respectively.